

Notice of References Cited	Application/Control No. 10/719,550	Applicant(s)/Patent Under Reexamination YANG ET AL.	
	Examiner Kin-Chan Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2004/0038840	02-2004	Lee et al.	510/202
	B	US-2003/0228990	12-2003	Lee et al.	510/202
	C	US-6,869,921	03-2005	Koito et al.	510/176
	D	US-6,503,840	01-2003	Catabay et al.	438/694
	E	US-2001/0014534	08-2001	Aoki et al.	438/689
	F	US-6,278,129	08-2001	Sugasawara et al.	257/48
	G	US-6,103,615	08-2000	Sugasawara et al.	438/622
	H	US-5,780,406	07-1998	Honda et al.	510/175
	I	US-6,149,828	11-2000	Vaartstra, Brian A.	216/57
	J	US-5,972,123	10-1999	Verhaverbeke, Steven	134/3
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf et al., Silico Processing for the VLSI Era, 1986, Vol. 1, page 517.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.